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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

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Monroe, Donald

Case: **15-6-9**

Serial No.: **09/648164**

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Examiner: **Dickey** **Group Art Unit: 2826**

Title: **Architecture for Circuit Connection Of A Vertical Transistor**

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231-

SIR:

Amendment Under 37 C.F.R.§1.111

In response to the office action of October 22, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

1. (Amended) An integrated circuit structure comprising:

a semiconductor layer having a major surface formed along a plane;

first and second spaced-apart doped regions extending into the surface from the plane;

a third doped region of different conductivity type than the first region, positioned above the plane and over the first region; and

a conductive layer formed between the first and second regions and above the plane, providing electrical connection between the doped regions.